

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) An interface in a Radio Base Station for transmission and reception of user data to and from one or more user equipments in a radio communication network, comprising:
a plurality of links having a minimized bandwidth for carrying data independent of the functionality of the radio access network and the airborne radio transmission; and,
one or more user data links for uplink and downlink, a control and supervision link, and a synchronization link; link;
wherein the synchronization link includes a frequency distribution, a time distribution and an interface delay calibration.
2. (Cancelled).
3. (Previously Presented) The interface according to claim 1, wherein said interface carries baseband signals comprising digital signal components that describe the airborne signal.
4. (Previously Presented) The interface according to claim 3, wherein the user data link transfers the downlink user data as symbols and the uplink user data as sampled symbols.
5. (Previously Presented) The interface according to claim 1, wherein the user data link carries information about stream identity for routing and/or supervision.
6. (Previously Presented) The interface according to claim 1, wherein the control and supervision link is split between a processor based link and fast indications.

7. (Previously Presented) The interface according to claim 6, wherein the fast indications are used to determine the status of the radio transmission part when the processor based link has failed.
8. (Previously Presented) The interface according to claim 6, wherein an indication is used to reset the radio transmission part.
9. (Previously Presented) The interface according to claim 1, wherein the synchronization link is used to control the transmission time of the user data link.
10. (Previously Presented) The interface according to claim 1, wherein the synchronization link is used to time stamp the reception time of the user data link.
11. (Previously Presented) The interface according to claim 6, wherein a hardware reset is encoded in the processor based link layer 1 protocol as a code violation.
12. (Previously Presented) The interface according to claim 5, wherein transmission of parity bits is suspended during stream identity transmission.
13. (Previously Presented) The interface according to claim 4, wherein the uplink data format consists of a fast changing mantissa and a slow changing exponent.
- 14-15. (Cancelled)
16. (Previously Presented) The interface according to claim 1, wherein the uplink interface serializer is controlled by the synchronization link.

17. (New) The interface according to claim 1 wherein a frequency is distributed as a bit clock of the interface.

18. (New) The interface according to claim 1 wherein the time distribution includes a time strobe transferred over the interface.

19. (New) The interface according to claim 1 wherein the interface delay calibration fine-tunes a downlink transmitter diversity and an uplink signal combination.